Amendments to the claims:

1 (canceled)

2 (original): A digital power amplifier comprising:

an analog amplifier which amplifies an input analog signal;

a low-pass filter including a coil and a first capacitor; ~~and~~ NW 7/1/05

a digital amplifier block which converts the output of said analog amplifier to a PWM signal, and controls power supply to said low-pass filter; and NW 7/1/05

~~wherein~~ a series circuit comprising a second capacitor and a resistance is applied as a feedback circuit which feeds-back a node voltage between the coil and the first capacitor of said low-pass filter to said analog amplifier, and the series circuit has a damper function for damping a high pass peak in the frequency response characteristic of said low-pass filter, which occurs when a load is not connected to said low-pass filter, or a high impedance load is connected thereto. NW 7/1/05

3 (new): A digital power amplifier as claimed in claim 2 wherein said digital amplifier block includes:

a PWM generator; and

a switching section controlled by the PWM generator wherein the switching section has a first switching element, a first coil, a second coil and a second switching element, connected in series in this order, between a high potential power supply line and a low potential power supply line;